

STORAGE CAPACITOR DESIGN FOR A SOLID STATE IMAGER

ABSTRACT OF THE DISCLOSURE

[0022] Storage capacitor design for a solid state imager. The imager includes several pixels disposed on a substrate in an imaging array pattern. Each pixel includes a photosensor coupled to a thin film switching transistor. Several scan lines are disposed at a first level with respect to the substrate along a first axis and several data lines are disposed at a second level along a second axis of the imaging array. Several data lines disposed at a second level with respect to the substrate along a second axis of the imaging array pattern. Each pixel comprises a storage capacitor coupled parallel to the photosensor, the storage capacitor comprising a storage capacitor electrode and a capacitor common electrode.